

10/03/00 09/005841 10/03/00

Class	Subclass
<b>ISSUE CLASSIFICATION</b>	

## U.S. UTILITY Patent Application

<p><b>O.I.P.E.</b></p> <p>SCANNED <u>5852</u> Q.A. <u>UO</u></p>	<p><b>PATENT DATE</b></p>
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APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER
09/805841	F	205	10T	132	Leagle
Toshiki Inoue Kyoko Kumagai		204	242	1742	Nicolan
Electrolytic plating method and device for a wiring board					

[illegible]

<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>		<b>CLAIMS ALLOWED</b>	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner)		<b>NOTICE OF ALLOWANCE MAILED</b>  _____	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____  _____  _____	_____ (Primary Examiner)		<b>ISSUE FEE</b>  Amount Due      Date Paid	
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.	_____ (Legal Instruments Examiner)		<b>ISSUE BATCH NUMBER</b>	
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